

	Hits	Search Text	DBs
1	127	exit\$3 with power with memory	USPAT; US-PGPUB
2	2	(exit\$3 with power with memory) and acpi	USPAT; US-PGPUB
3	0	(exit\$3 with power with memory) and (current adj control adj register)	USPAT; US-PGPUB
4	22	(exit\$3 with power with memory) and bios	USPAT; US-PGPUB
5	3	(current adj control adj register) with memory	USPAT; US-PGPUB
6	0	(current adj control adj register) and (memory adj core)	USPAT; US-PGPUB
7	14	(current adj control adj register) same memory	USPAT; US-PGPUB
8	6	(current adj control adj register) and (memory with initializ\$5)	USPAT; US-PGPUB
9	23	(current adj control adj register) and memory	USPAT; US-PGPUB
10	1	ich with messag\$3 with memory	USPAT; US-PGPUB
11	26	hub with messag\$3 with memory	USPAT; US-PGPUB
12	8	control with hub with messag\$3 with memory	USPAT; US-PGPUB
13	4692	memory with (low adj power)	USPAT; US-PGPUB
14	316	memory with (low adj power adj (state or mode))	USPAT; US-PGPUB
15	23	memory with (low adj power) with exit\$4	USPAT; US-PGPUB
16	12	memory with (low adj power) with resum\$4	USPAT; US-PGPUB
17	4	(memory with (low adj power adj (state or mode))) same acpi	USPAT; US-PGPUB
18	23	(memory with (low adj power adj (state or mode))) and s1	USPAT; US-PGPUB
19	3	(memory with (low adj power adj (state or mode))) and (acpi same s1)	USPAT; US-PGPUB
20	19	memory with (low adj power) with initializ\$5	USPAT; US-PGPUB
21	47	(memory with (low adj power adj (state or mode))) same detect\$3	USPAT; US-PGPUB
22	3	(memory with (low adj power) with exit\$4) same detect\$3	USPAT; US-PGPUB
23	6	(memory with (low adj power)) same detect\$3 same exit\$4	USPAT; US-PGPUB
24	1078	memory with initializ\$5 with command\$1	USPAT; US-PGPUB
25	0	memory with (initializ\$5 adj command\$1) with resum\$5	USPAT; US-PGPUB

	Hits	Search Text	DBs
26	127	memory with (initializ\$5 adj command\$1)	USPAT; US-PGPUB
27	60	memory with (initializ\$5) with resum\$5	USPAT; US-PGPUB
28	10	(memory with (initializ\$5) with resum\$5) and rambus	USPAT; US-PGPUB
29	2	(memory with (initializ\$5 adj command\$1)) and rambus	USPAT; US-PGPUB
30	1267	memory with initializ\$5 with instructions	USPAT; US-PGPUB
31	306	memory with resum\$5 with instructions	USPAT; US-PGPUB
32	15	(memory with initializ\$5 with instructions) and (memory with resum\$5 with instructions)	USPAT; US-PGPUB
33	44	memory with resum\$5 with instructions with mode	USPAT; US-PGPUB
34	86	memory with resum\$5 with instructions with (mode or state)	USPAT; US-PGPUB
35	20	memory with resum\$5 with instructions with (mode or state) with power	USPAT; US-PGPUB
36	2	rambus same wak\$3	USPAT; US-PGPUB
37	1	"5319601".PN.	USPAT
38	0	"5781782".PN.	USPAT
39	1	"5781782".PN.	USPAT
40	0	"5828619".PN.	USPAT
41	1	"5828619".PN.	USPAT
42	12	memory with initializ\$5 with (power adj (mode or state))	USPAT; US-PGPUB
43	2	memory with re\$1initializ\$5 with (power adj (mode or state))	USPAT; US-PGPUB
44	41	"5262998"	USPAT; US-PGPUB
45	43	current adj control adj register	USPAT; US-PGPUB
46	4	(current adj control adj register) same midpoint	USPAT; US-PGPUB
47	13	(current adj control adj register) and rambus	USPAT; US-PGPUB
48	8	(current adj control adj register) and rdram	USPAT; US-PGPUB
49	0	(current adj control adj register) and rambus	EPO; JPO; IBM_TDB

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50	0	(current adj control adj register) and rdram	EPO; JPO; IBM_TDB
51	1	memory with initializ\$5 with (power adj (mode or state))	EPO; JPO; IBM_TDB
52	1	memory with (low adj power) with exit\$4	EPO; JPO; IBM_TDB
53	0	memory with (low adj power) with resum\$4	EPO; JPO; IBM_TDB
54	6	memory same (low adj power) same resum\$4	EPO; JPO; IBM_TDB
55	0	(memory same (low adj power)) and rdram	EPO; JPO; IBM_TDB
56	0	(memory and (low adj power)) and rdram	EPO; JPO; IBM_TDB
57	1	(memory and (low adj power)) and rambus	EPO; JPO; IBM_TDB
58	56	(memory and (low adj power)) and dram	EPO; JPO; IBM_TDB
59	38	(memory same (low adj power)) and dram	EPO; JPO; IBM_TDB
60	0	(memory same (low adj power)) and dram and (resum\$5 or exit\$5)	EPO; JPO; IBM_TDB
61	36	(set\$4 near bit) with (power adj (mode or state))	USPAT; US-PGPUB